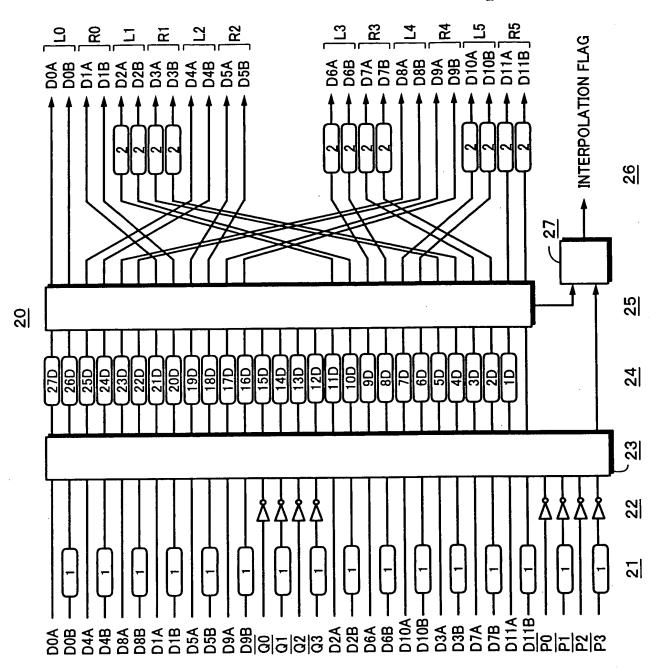


Fig. 1

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Fig. 2

OBLON ET AL (703) 413-3000 DOCKET # 2572/114\(\Sheet\) OF 14 10/511271 207 1 1 V 1 V. 16 ββ 5 P2 P1 P3 P3 P3 10S **D=**2 -187 -190 -160 -151 12 92 93 93 -123 -124 -144 -187 -187 -129 -132 -157 -160 -175 -178 -181 D68 D7A D7B D7B D8B D9A D10A D11A D11B D0A D0B D1A D1B D2A D2B D3B D4A D4B D5A D5B

Fig. 3

**L4** 

7

R2

2

2

쮼

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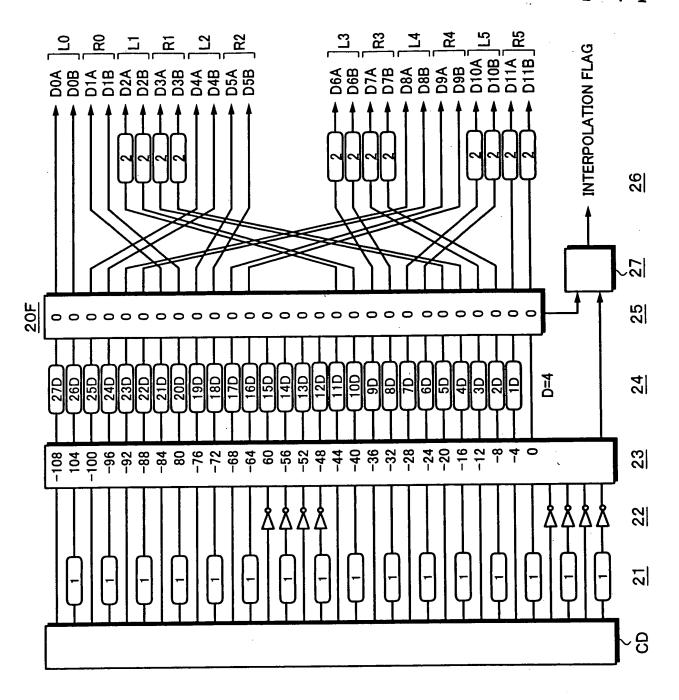


Fig. 4

Fig. 5

DATA	LO		R	0	L	1	R	1	L	2	R2	
SYMBOL FRAME	D0B	D0A	D1B	D1A	D2B	D2A	D3B	D3A	D4B	D4A	D5B	D5A
-190												
-187												
-184												
-181												
-178						Ĺ <u>.</u>	S20					
-175								S19				
-172												
-169									·	ļ		
-166									<u> </u>			
-163					<u> </u>						<b></b>	
-160					S14							
-157						S13				ļ	ļ	
-144							ļ	ļ	L	<b> </b>	ļ	
-141									ļ	ļ		
-138							ļ	<u> </u>		ļ	S10	
-135					<u> </u>	<u> </u>		ļ	<u> </u>	<u> </u>	ļ	S9
-132			S8				<u> </u>	ļ			<u> </u>	
-129				S7				ļ	ļ	ļ	<u> </u>	
-126				·		<u> </u>		<u> </u>				<u> </u>
-123						<u> </u>	ļ	<u> </u>				ļ
-120									S4	ļ	ļ	
-117						<u> </u>		ļ	ļ	S3	ļ	
-114	S2						<u> </u>	ļ		ļ		ļ
-111		S1	<u>                                     </u>	<u> </u>	<u> </u>	<u> </u>	<u> </u>	<u> </u>				

Fig. 6

DATA	L3		R	3	L	4	R	4	L5		R5	
SYMBOL FRAME	D6B	D6A	D7B	D7A	D8B	D8A	D9B	D9A	D10B	D10A	D11B	D11A
-190											S24	
-187												S23
-184			S22									
-181				S21								
-178												
-175												
-172									S18	<u> </u>		
-169										S17		
-166	S16										<u> </u>	
-163		S15										
-160												
-157												
-144							S12		ļ		<u></u>	
-141				<u></u>				S11	<u> </u>		<u> </u>	
-138									<u> </u>	<u> </u>	ļ	
-135				<u> </u>	<u> </u>				ļ	ļ	<u> </u>	
-132			<u> </u>					<u> </u>		<u> </u>		
-129									1	<u> </u>		<u> </u>
-126					S6	<u> </u>		<u> </u>			<del>                                     </del>	
-123						S5	ļ	<u> </u>	<u> </u>			<u> </u>
-120						<u> </u>	<u> </u>		<u> </u>			<u> </u>
-117							<b> </b>	<u> </u>	<u> </u>	<u> </u>	ļ	<u> </u>
-114							<u> </u>	<u> </u>	ļ	<u> </u>	<b></b>	<u> </u>
-111					<u> </u>	<u> </u>	<u> </u>		<u> </u>			<u> </u>

Fig. 7

DATA	LO		R0		L1		R	1	L	2	R2	
SYMBOL FRAME	D0B	D0A	D1B	D1A	D2B	D2A	D3B	D3A	D4B	D4A	D5B	D5A
-156	S2	S1	S8	<b>S</b> 7					S4	S3	S10	S9
-154					S14	S13	S20	S19				
-153	S2	S1	S8	S7					S4	S3	S10	S9
-151					S14	S13	S20	S19				
-150	S2	S1	S8	<b>S</b> 7					S4	S3	S10	S9
-148	1				S14	S13	S20	S19				
-147	S2	S1	S8	S7					S4	S3	S10	S9
-145					S14	S13	S20	S19			<u> </u>	

Fig. 8

DATA	LO		R0		L1		R	:1		2	R2	
SYMBOL FRAME	D6B	D6A	D7B	D7A	D8B	D8	D9B	D9A	D10B	D10A	D11B	D11A
-156					S6	S5	S12	S11				
-154	S16	S15	S22	S21					S18	S17	S24	S23
-153					S6	S5	S12	S11				
-151	S16	S15	S22	S21					S18	S17	S24	S23
-150					S6	S5	S12	S11				
-148	S16	S15	S22	S21					S18	S17	S24	S23
-147					S6	S5	S12	S11				
-145	S16	S15	S22	S21					S18	S17	S24	S23

Fig. 9

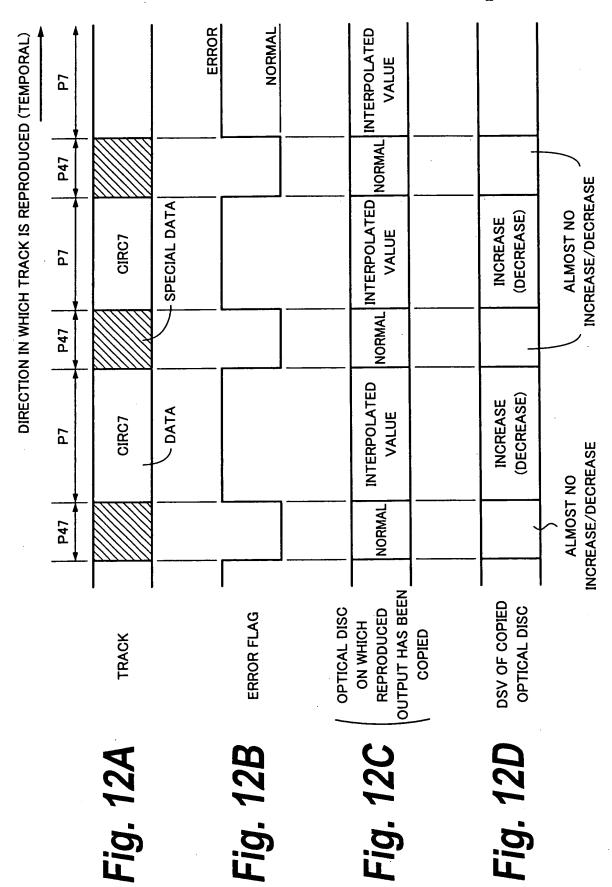
DATA	L	L0		LO RO		L	L1		R1		L2		2
SYMBOL FRAME	DOB	D0A	D1B	D1A	D2B	D2A	D3B	D3A	D4B	D4A	D5B	D5A	
0	S2	S1	S8	<b>S</b> 7					S4	S3	S10	S9	
1													
2					S14	S13	S20	S19	<u> </u>			<u> </u>	

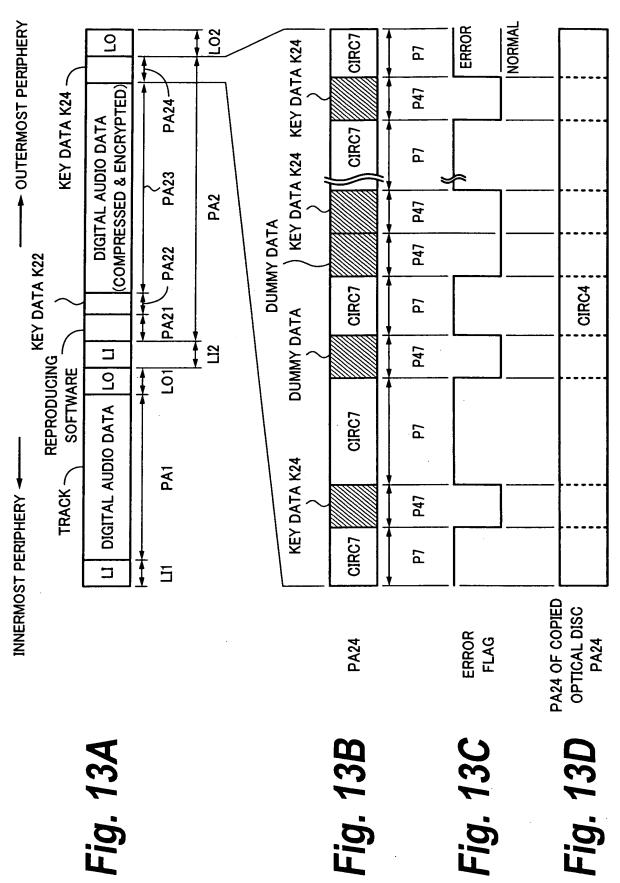
Fig. 10

1	DATA	L3		R3		L4		R	R4		L5		5
	SYMBOL FRAME	D6B	D6A	D7B	D7A	D8B	D8A	D9B	D9A	D10B	D10A	D11B	D11A
ľ	0					S6	S5	S12	S11				
ľ	1												
ľ	2	S16	S15	S22	S21					S18	S17	S24	S23

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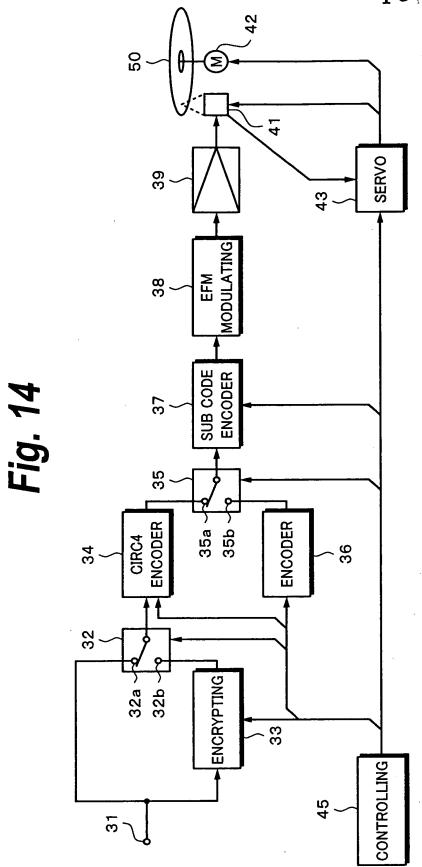
		ONE SYMBOL PERIOD ONE	ONE SYMBOL PERIOD
Fig. 11A	ORIGINAL DATA	1 0 0 1 0 0 1 0 (92h) 1 0 (	1 0 0 1 0 0 1 0 (92h)
; ;		CONNECTION BITS CONN	CONNECTION BITS
Fig. 11B	CHANNEL BITS	1001001000001 *** 1001001000001 ***	1001000001***
7.7		000	000
۲. اق.	CONNECTION BITS	010	010
	ļ		, ,
rig. Til	CHANNEL BITS	10010010000010001000100000000001	100110000010001
Fig. 11E	DSV	+3 -3 +7 -4 +3	<del>-3   +7   -4                                   </del>
		DSV=+3	DSV=+3



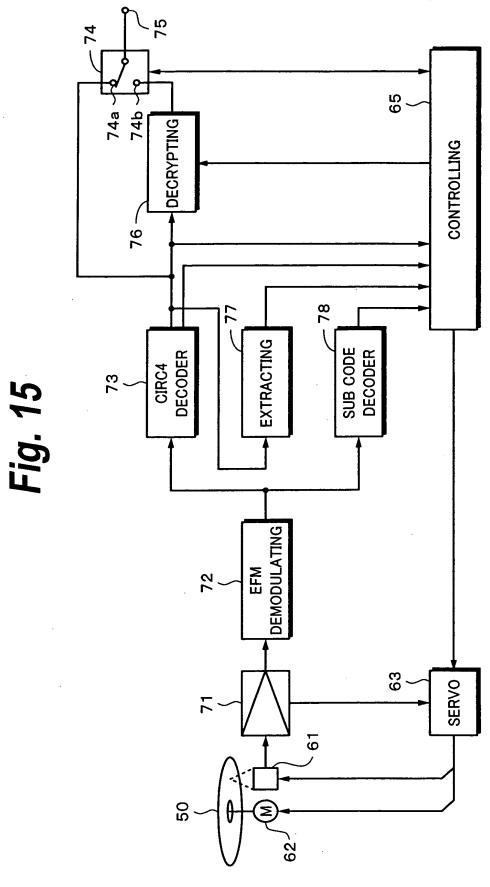


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## DESCRIPTION OF REFERENCE NUMERALS

35

10	CIRC ENCODER CIRCUIT
108	CIRC7 ENCODER CIRCUIT
20	CIRC DECODER CIRCUIT
20F	CIRC4 DECODER CIRCUIT
33	ENCRYPTING CIRCUIT
34	CIRC4 ENCODER CIRCUIT
36	CIRC7 ENCODER CIRCUIT
37	SUB CODE ENCODER CIRCUIT
38	EFM MODULATING CIRCUIT
41	RECORDING OPTICAL HEAD
50	OPTICAL DISC
61	REPRODUCING OPTICAL HEAD
72	EFM DEMODULATING CIRCUIT
73	CIRC4 DECODER CIRCUIT
76	DECRYPTING CIRCUIT
77	CIRC7 DECODER CIRCUIT
78	SUB CODE DECODER CIRCUIT